IN THE SPECIFICATION

Please amend paragraph [0023] as indicated below:

[0023]   Fig. 3 shows an example of the structure of the memory register 21.   As shown in Fig. 3, the memory register 21 includes addresses "00" to "±1F" (where "--" represents a hexadecimal value).   Each address includes a 2-byte read register and a 2-byte write register.   Information in the electronic device 200 is read into the read register, whereas information from the main unit 100 is written in the write register.